TOSHIBA RF POWER AMPLIFIER MODULE

S-AV40

FM RF POWER AMPLIFIER MODULE FOR 30W COMMERCIAL VHF RADIO APPLICATIONS

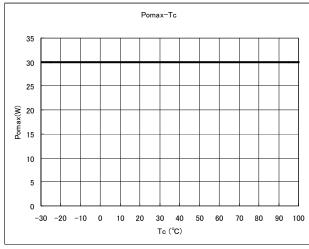
Power Gain: 34.7 dB (Min.)Total Efficiency: 40% (Min.)

ABSOLUTE MAXIMUM RATINGS (Tc = 25°C, I_T < 8 A, Z_G = Z_L = 50 Ω)

CHARACTERISTICS	SYMBOL	TEST CONDITION	RATING	UNIT
Maximum Current	Ι _Τ		8	А
Power Supply Voltage	V_{DD}	V _{GG} = 0 V (GND), RF: none	16.5	V
Control Voltage	V _{GG}	$10.5 \le V_{DD} \le 16.5 \text{ V, Pi} = 50 \text{ mW}$	5.5	V
Instantaneous Output Power	Pomax	$V_{GG} \leq$ 5.5 V, Pi = 50 mW, 10.5 \leq $V_{DD} \leq$ 16.5V, within 2 seconds	40	W
Input Power	Pi	$10.5 \le V_{DD} \le 16.5 \text{ V}, V_{GG} \le 5.5 \text{ V}$	100	mW
Operating Case Temperature	T _{c (opr)}	$10.5 \le V_{DD} \le 16.5$ V, $V_{GG} \le 5.5$ V, Pi = 50 mW (Note 2)	-30 to 100	°C
Storage Temperature	T _{stg}		-40 to 110	°C

Note 1: The maximum ratings are the limits that must not be exceeded even for an instant, under worst possible conditions. Exceeding the ratings may cause device damage, ignition, or deterioration. Therefore, when designing the circuitry, derating factors should be applied so that the absolute maximum ratings are not exceeded.

Note 2: The output power rating satisfies the range shown in Figures 1 and 2 according to the operating case temperature. Ensure that the device should be operated within the specified operating range. The figures below indicate the output power obtained 2 seconds after Po is generated.



PDmax-Tc 100 90 80 60 PDmax(W) 50 40 Power dissipation (PD) 30 $PD = (VDD \times IDD) - Po + Pi$ 20 10 -30 -20 -10 0 30 40 50 60 70 80 90 100 10 20 Tc(°C)

Figure 1 Pomax-Tc

Figure 2 PDmax-Tc

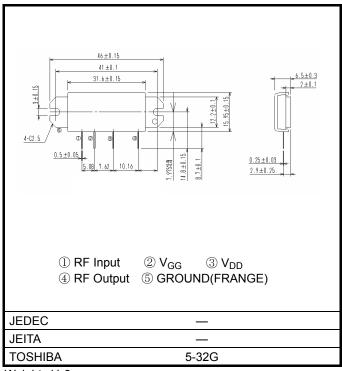
*When the device is used at $Tc = 100^{\circ}C$, the output power rating is 30 W as shown in Figure 1. When the power dissipation at $Tc = 100^{\circ}C$ exceeds the rating shown in Figure 2, the output derating is required to limit the dissipation within the specified range.

Note 3: The case temperature is monitored using the screw terminal blocks on the input side that are used for the module implementation.

Note 4: To protect a device from being permanently damaged, the power-on sequence must be as follows (, while the reversed order should be applied when turning off): 1. VDD, 2. Pi, 3. VGG

PACKAGE OUTLINE

Unit in mm



Weight: 11.8g

ELECTRICAL CHARACTERISTICS (Tc = 25°C, $Z_G = 50\Omega$)

CHARACTERISTICS	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Frequency Range	f _{range}	_	220	_	246	MHz
Output Power	Po	V_{DD} = 12.5 V V_{GG} = 5 V Pi = 50 mW Z_{L} = 50 Ω	30	_	_	W
Power Gain	Gp		34.7	_	_	dB
Total Efficiency	ηT		40	_	_	%
Input VSWR	VSWRin		_	_	3.0	_
Second Harmonic	2nd HRM		-	_	-25	dB
Third Harmonic	3rd HRM		-	_	-30	dB
Ruggedness	_	$\begin{array}{l} 10.5~\text{V} \leq \text{V}_{DD} \leq 16.5~\text{V},~0~\text{V} \leq \text{V}_{GG} \leq \text{V}_{GGajs} \\ (\text{V}_{GG} = \text{V}_{GGajs} \ @ \ \text{Po} = 30~\text{W}) \\ \text{Pi} = 50~\text{mW} \\ \text{P}_0 = 30~\text{W}~(\text{Adjusted via V}_{GG} \ @ \ Z_L = 50\Omega) \\ \text{VSWR LOAD 20: 1 ALL PHASE (@ 2 s)} \end{array}$	N	_		
Stability	_	$\begin{array}{l} 10.5~\text{V} \leq \text{V}_{DD} \leq 16.5~\text{V},~0~\text{V} \leq \text{V}_{GG} \leq \text{V}_{GGajs} \\ (\text{V}_{GG} = \text{V}_{GGajs} \ @ \ \text{Po} = 30~\text{W}) \\ \text{Pi} = 50~\text{mW} \\ \text{Po} \leq 30~\text{W}~\text{(Adjusted via V}_{GG} \ @ \ Z_L = 50\Omega) \\ \text{VSWR LOAD 3: 1 ALL PHASE} \end{array}$	No s of -60	_		

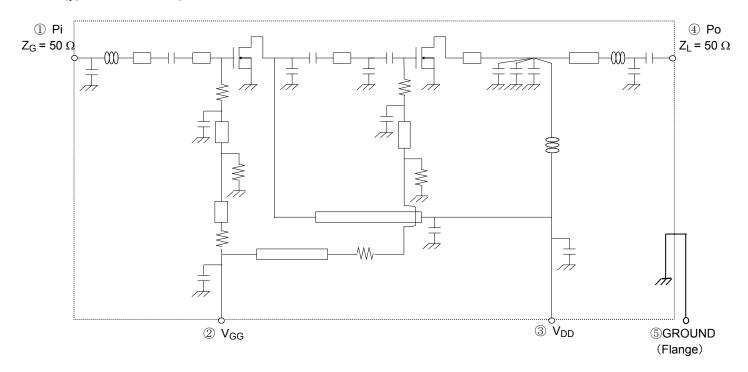
Note 5:The output power is intended to follow the rating provided in Figure 1 in Note 2. Note 6: Stability

Measurements are performed under the conditions where VSWR is at 3:1 through all phases over the whole frequency range, and they are guaranteed only under those conditions. Even though it is guaranteed to be stable where VSWR is at 3:1, the VSWR load over the operating frequency should be designed to be 50 Ω . At the same time, ensure that the VSWR load does not deviate much from 50Ω even for a moment, nor deviate even a little from 50Ω continually. The S-AV40 is not intended for such operations, and proper operation under such conditions is not guaranteed due to the possibilities of heat generation in the module and its applications.

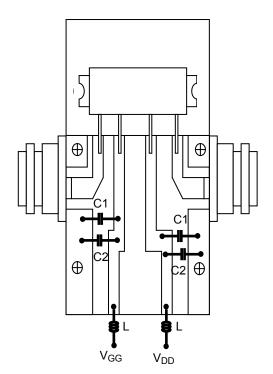
HANDLING PRECAUTIONS

- Since this product has a protective cap, care should be taken to avoid applying an excessive impact and allowing
 foreign objects to get inside when handling this product. Also, please do not remove a cap. If the cap is removed,
 the foreign object inside the module or the applied impact may lead IC failure, causing smoke or ignition.
- Since this product is structurally susceptible to static electricity, protections against the static electricity should be applied to objects that may come in direct contact with devices, such as worktables, equipment, operators and solder irons.
- This product is not designed nor intended to perform a continuous transmission for applications like a base station. Please do not use this product for such applications, for the reliability cannot be guaranteed.
- This product is intended to be used for a single operation (single-device operation). A proper operation is not guaranteed for a parallel operation. A parallel operation should be performed in accordance with your own good judgment.
- · Mounting method
 - \cdot The flatness of a heat sink must not exceed 50 μ m. If the flatness exceeds 50 μ m, the device may experience an unexpected stress that may lead to module breakdown due to damage or ignition in the substrate inside a module and other module parts.
 - · Please apply thermal compound between a module and a heat sink to improve the adhesive property.
 - Use a 4-mm diameter screw with the clamping screw torque of 1.2 to 1.5 Nm.
 - · Please solder the module leads after the screw is clamped.

EQUIVALENT CIRCUIT



TEST FIXTURE



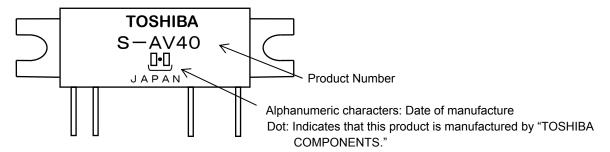
C1: 10000 pF

C2: 10 μF

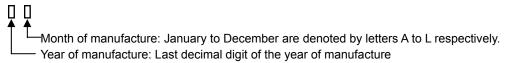
4

L: $\phi 0.5\text{-mm}$ enamel wire, 4T, 3ID

MARKING



Explanation of Lot No.



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20070701-EN GENERAL

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